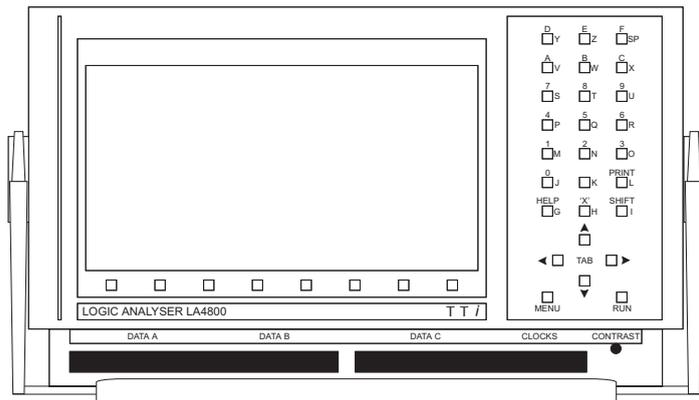


LA3200 & LA4800 32 & 48 channel Logic Analysers



- 32 channels (LA3200) or 48 channels (LA4800)
- 25MHz synchronous operation on all channels
- 100MHz asynchronous operation (8 or 12 channels)
- 5ns glitch capture capability
- Multi-level trigger sequencing
- Non-volatile data and set-up memories
- Disassembler options for popular uPs

A specification far in excess of its price

The LA3200 and LA4800 have a specification that meets the needs of all but the most demanding of applications. 32 or 48 data channels, 100MHz maximum acquisition speed, 5ns glitch capture, multi-level trigger sequencing, and many other advanced features.

A big screen in a compact package

The LA3200 and LA4800 have a 9 inch high resolution LCD screen which uses the latest cold-cathode fluorescent technology to give superb viewability in all lighting conditions. Despite this large screen, the instruments are considerably smaller and lighter than other logic analysers thus reducing bench space and improving portability.

Hard-copy output and remote interfaces

Both units have an RS-423 serial interface (RS-232 compatible) as standard. This enables data and set-up information to be transferred to and from other equipment. An IEEE-488 interface with the same capabilities is optional.

Built-in printer driver routines enable listings and screen dumps to be printed out via the serial interface and a serial to centronics converter cable is available for use with parallel input printers.

More channels and a deeper memory

TTi offers you a choice of 32 or 48 data channels at DC to 25MHz to cope with the increasing complexity of today's digital equipment. Of equal importance is the memory depth which is a full 8K on all 32 or 48 channels

100MHz asynchronous acquisition

When making timing measurements you need as much speed as you can get, so TTi gives you a full 100MHz on 8 or 12 channels.

5 nanosecond glitch capture

Glitch capture allows you to see very short pulses which are beyond the resolution of the analyser's sample clock. TTi enable you to detect glitches down to 5ns on 16 or 24 channels and provide glitch trigger. Its an important facility for finding some types of fault.

Multiple clocks for complex synchronous capture

The LA3200 and LA4800 have three external clock inputs each with its own qualifier selectable for active edge and active level respectively. This gives highly flexible clocking for synchronous data capture from DC up to 25MHz.

Multi-level trigger sequencing

The most important requirement of a logic analyser is that it should capture exactly the data that you need. That requires sophisticated triggering. TTi give you a four step sequencer with event counting and redirection on each step allowing skip and restart. Trigger words are full width and can include NOT words.

Note: This is a faxable data sheet, a colour brochure is also available.

Full width trace control and data qualification

The instruments provide full width trace start and trace stop words to control the recording of data. By defining the trace stop word to be the NOT of the trace start word full width data qualification is provided. Without it you could be filling the memory with unwanted data.

Free-format state listings

The state listing screen can be formatted exactly how you want it. Multiple groups can be defined with groups of channels appearing in differing radixes under user defined names. Input channels can be repeated within different groups enabling the same data to be displayed in several radixes simultaneously.

Powerful search and compare facilities

The analysers can search for a word or a block of any length in any memory. A block of any length and any position can be compared with with a block in any position in any memory using a bit mask and a skew filter. Acquisition can be started or stopped depending on the results of the comparison enabling an automatic 'fault-finder' mode to be set up.

Non-volatile data and set-up memories

Multiple non-volatile reference memories are provided for storing data. Each can be annotated with a user description. Fourteen non-volatile set-up memories are provided, also with user annotation, so that the complete configurations for a number of jobs can be stored permanently.

Easy connection to your target equipment

Combination data pods (AP01 for 32 channels and AP02 for 48 channels) provide a low cost solution by combining all the data and clock inputs on a single pod. These provide state or timing analysis up to 25MHz.

High Speed data pods group the data channels into blocks of 16 and provide 25MHz state, 100MHz timing and Glitch Capture. A separate clock pod is needed. These pods are available in both fixed threshold (AP03) and variable threshold (AP03V) versions with corresponding clock pods (AP04 or AP04V).

LA4800 additional features

The additional 16 channels of the LA4800 give it extra 'future proofing'. 48 channels provide for the analysis of more complex hardware systems and are essential for the disassembly of microprocessors such as the 68000.

Extra reference memories

The LA4800 has four non-volatile reference memories (the LA3200 has two).

Performance analysis

The LA4800 provides statistical analysis which is very valuable in analysing the performance of a digital system. A histogram can be generated for a group of channels over a defined number of acquisitions. The histogram shows the occurrence of states within each of up to 8 user specified ranges as an absolute number and as a percentage.

Signature analysis

The LA4800 provides signature analysis which is very useful for the Go - NoGo testing of digital equipment. It enables a section of data within any of the memories to be compressed into a 4 digit hex 'signature'. Signatures are available per channel, per group of channels and per memory, and can be automatically compared with signatures from another memory.

Analog display mode

The LA4800 provides an analog display mode whereby the analog equivalent of an 8 bit digital word from the input channels is displayed. This facility can be used to reconstruct any digitised analog information such as the output of an analog to digital converter.

A wide range of disassemblers

Disassembler pods with built-in firmware are available for a wide range of popular 8-bit and 16-bit microprocessors. Because the disassembler firmware is incorporated in the pod, disassemblers can be interchanged at will.

Thurlby Thandar Instruments Ltd. operates a policy of continuous development and reserves the right to alter specifications without prior notice.

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